

FIG. 1

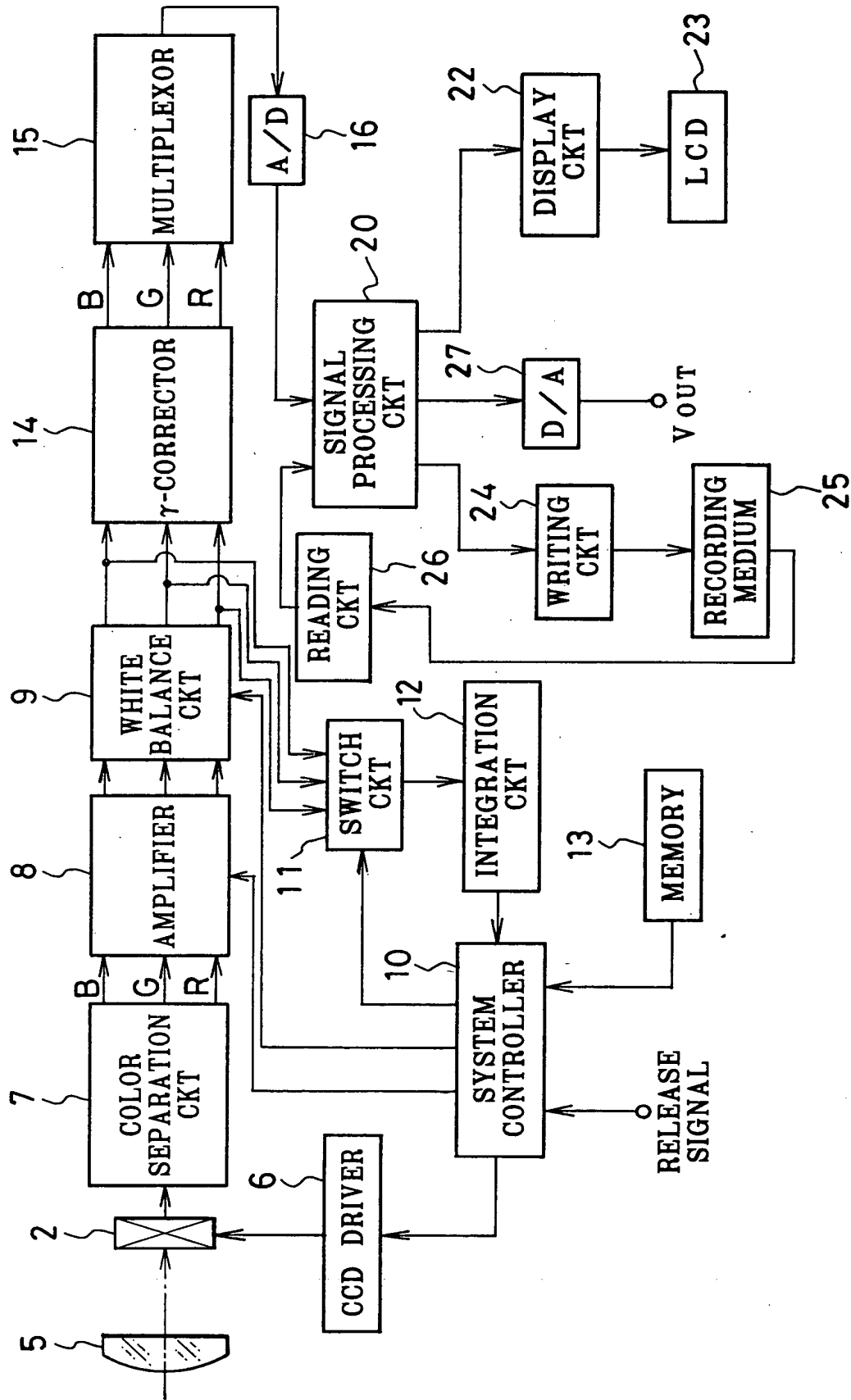


Diagram illustrating a color filter 2, showing the arrangement of subpixels (4B, 4G, 4R) and the addition of light (ADD) for the ODD FIELD and EVEN FIELD.

The diagram illustrates the timing sequence for the video system. It consists of six horizontal tracks:

- SHUTTER RELEASE**: A single pulse at the beginning of the sequence.
- VSYNC**: A series of vertical sync pulses. The first three are labeled  $T_1$  and the next two are labeled  $T_2$ .
- CHARGE STORAGE TIME**: A signal that is high during the  $T_1$  and  $T_2$  intervals, indicating when the CCD is accumulating charge.
- CCD OUT**: A signal that is high during the  $T_1$  and  $T_2$  intervals, indicating when the CCD is outputting data. Arrows point from the  $T_1$  and  $T_2$  labels to the corresponding high periods of this signal.
- LCD**: A signal that is high during the  $T_1$  and  $T_2$  intervals, indicating when the LCD is displaying the image. Arrows point from the  $T_1$  and  $T_2$  labels to the corresponding high periods of this signal.
- IMAGE SIGNAL RECORDING**: A signal that is high during the  $T_1$  and  $T_2$  intervals, indicating when the image signal is being recorded. An arrow points from the  $T_2$  label to the corresponding high period of this signal.

FIG. 4

